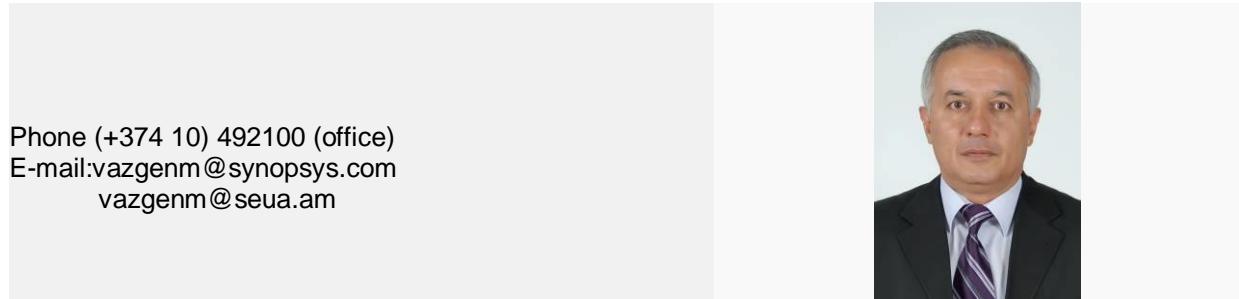# VAZGEN SHAVARSH MELIKYAN

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Education
Corresponding Member of National Academy of Sciences of the Republic of Armenia, Yerevan, RA, 2014
Diploma of Academician of Academy of Sciences of Applied Radio Electronics of Russia, Ukraine and Belarus, Ukraine, 2012
Academic rank of Professor in Automated Systems, SEUA, Yerevan, 2006
Doctor of Technical Sciences, SEUA, Yerevan, 2006
Academic rank of Associate Professor in Computer Science Supreme Certification Board, Moscow, 1985
PhD, Moscow Engineering-Physics Institute (MIFI), Moscow, 1984
Diploma of Excellence in Computer Science, Polytechnic Institute, Yerevan, 1978
Attestat in No 3 Secondary School, Yerevan, 1973

Awards
- Highest award, the gold medal of the Ministry of Education and Science of Armenia for "the greatest contribution to the field of education and science", Yerevan, Armenia, 2016
- Gold Medal of Yerevan State University for significant contribution in the field of University Education and Science and the long-term fruitful cooperation with Yerevan State University, Yerevan, Armenia, 2016
- Gold Medal of the Russian-Armenian (Slavonic) University for many years of close cooperation, training highly qualified specialists in "Design and Technology of Electronic Means" specialization, Yerevan, Armenia, 2016
- Medal of Regional Educational Academy, Yerevan, Armenia, 2016
- Gold Medal of VivaCell-MTS for significant contribution in the field of Education and Science, Yerevan, Armenia, 2016
- Acknowledgement of Gratitude for "Efficient Activities and Participation of Works of Public Council by the Ministry of Defense of RA, Yerevan, RA, 2015
- Honorary Professor of Artsakh State University for significant contribution in the field of Science and Education, productive activities and scientific connections with the Artsakh State University, 2015
- "Vazgen Sargsyan" Medal of the Ministry of Defense of RA, order of the Minister of Defense of RA No 93 01.02.2013, Yerevan, RA, 2013
- "Garegin Nzhdeh" Medal of the Ministry of Defense of RA, order of the Minister of Defense of RA No 966 14.09.2012, Yerevan, RA, 2012
- Honorary Professor of National Research University MIET, decision of Scientific Board of MIET of April 16, 2012, Moscow, Russia, 2012
- Jubilee medal "Military Forces of RA 20 Years" of the Ministry of Defense of RA, order of the Minister of Defense of RA No 1537 24.12.2011, Yerevan, RA, 2011
- Gold medal of State Engineering University of Armenia, university administration decision No 01/03-1 06.10.2011, order No 01-03/456 10.10.2011, certificate 0210, Yerevan, RA, 2011
- Diploma of Yerevan State University "For Achievements in Science and Significant Contribution in University Education Development", Yerevan, RA, 2011
- Honorable Medal of European Regional Educational Academy 10-year Anniversary, order No 8, Yerevan, RA, 2011
- "President of the Republic Prize" in "Technical Sciences and Information Technologies" area, Yerevan, RA, 2010
- Best Paper Award in the 8th IEEE East-West Design & Test International Symposium, St. Petersburg, Russia, 2010
- Diploma for Outstanding Training of Excellent student-Experts in Design and Test of Computer Systems", Open International Student Olympiad "Technical Diagnosis of Computer Systems and Networks", Kharkov, Ukraine, 2010
- Honorable Professor of European Regional Academy of Armenia, 2009
- Best Paper Award in the 7th IEEE East-West Design & Test International Symposium, Moscow, Russia, 2009
- Honorary Medal of Engineering Academy of Armenia, Diploma CM 020, Protocol 1 12.15.2008, Yerevan, RA, 2008
- Best Paper Award in the 6th IEEE East-West Design & Test International Symposium, Lvov, Ukraine, 2008

- Title of Honorable Scientist of the Republic of Armenia, Yerevan, RA, 2007
- 1st prize in SEUA Scientific Contest in the nomination of the Best Scientific Publication (research work), 2006-2007, Yerevan, RA, 2007
- Best Paper Award in the 5th IEEE East-West Design & Test International Symposium, Yerevan, RA, 2007
- Best Paper Award in Problems of Developing Advanced Microelectronic Systems Conference, Moscow, RF, 2006
- 1st prize in SEUA Scientific Contest in the nomination of the Best Scientific Publication (research work), 1998-1999, Yerevan, RA, 1999
- 1st prize in SEUA Scientific Contest in the nomination of the Best Master Theses Supervisor, Yerevan, RA, 1997
- Praise of SEUA Board of Trustees and Management, Yerevan, 1996
- Medal for the Best Diploma Work of Armenia, Yerevan, RA, 1978
- Lenin Scholarship awarded for academic excellence, Yerevan, RA, 1975-1978
- 2nd prize in all-union Mathematics Olympiad of USSR pupils, Kishinev, Moldova, 1973

Membership
- Member of Editorial Board of Journal of Electrical Engineering, David Publishing Company, 2016
- Member of Steering Committee of Small System Simulation Symposium (SSSS), February 12-14, Serbia, 2016
- Member of Technical Program Committee of the 3rd International Conference on Electrical, Electronic and Computing Engineering (ICETRAN 2016), Zlatibor, Serbia, 2016
- Member of Editorial Board for the Proceedings of the Republic of Armenia National Academy of Sciences and National Polytechnic University of Armenia, 2015-present
- Member of Editorial Board for the Microelectronics Journal of Elsevier Science, 2015-present
- Member of RAU Scientific Board, Yerevan, RA, 2015-present
- Member of Technical Program Committee of the 2nd International Conference on Electrical, Electronic and Computing Engineering (ICETRAN 2015), Silver Lake (Srebrno Jezero), Serbia, 2015
- Member of editorial staff of "Proceedings of universities. Electronics" journal of MIET, RF, Moscow, 2015-present
- Member of editorial staff of "Proceedings of TUSUR University journal published by Tomsk State University of Control Systems and Radioelectronics, RF, Tomsk, 2015-present
- Member of Steering Committee of Small System Simulation Symposium (SSSS), February 12-14, Serbia, 2014
- Member of Program Committee and keynote speech in the VI All-Russia Science&Technology Conference "Problems of Advanced Micro- and Nanoelectronic Systems Development" (MES2014), Moscow, 29 September 2014 - 3 October 2014
- Member of Program Committee and invited talk in the 12th East-West Design & Test Symposium (EWDTS 2014), Kiev, Ukraine, September 12 - 15, 2014
- Board Member and invited talk in Conference for Electronics, Telecommunications, Computers, Automation, and Nuclear Engineering – ETRAN, Serbia, 2014
- Member of editorial board of Journal High Technologies, Kiev, Ukraine, 2013
- Member and keynote speech in the Steering Committee of Asia-Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PrimeAsia), Hyderabad, India, December 19-21, 2013
- Member of Board of YSU IT Educational and Research Center, Yerevan, RA, 2011-present
- Member of Org. Committee of Annual Best IT Managers Award, Yerevan, Armenia, 2011
- Member of Program Committee of the 8th International Conference of "Semiconductor Micro- and Nanoelectronics", Yerevan, Armenia, 2011
- Co-chairman of "Scientific Research in Informatics in Universities" section of International conference "Education, Science and Economics at Universities. Integration to International Educational Area", Yerevan, Armenia, 2011
- Member of Program Committee of International Student Olympiad and competition of student's scientific works "Technical Diagnostics of Computer Systems and Networks" with the support of IEEE East-West Design & Test Symposium, Kharkov, Ukraine, 2011
- Academician of Engineering Academy of RA, Yerevan, RA, 2010-present
- Member of the RA Presidential Award Committee in the sector of science and technology - Yerevan, RA, 2010-present
- Honorable Member of National Kickboxing Federation of RA, Yerevan, RA, 2010 - present
- Executive Secretary of Global IT Awards of the President of RA, Yerevan, RA, 2009-2012
- Member of Public Board adjacent to Minister of Defense of RA, 2009-present
- Member of Program Committee of IEEE East-West Design & Test Symposium (EWDTS'09), Moscow, Russia, 2009
- Member of Program Committee of ArmTech'97 Congress, San Jose, CA, USA, 2009
- Head of Design and Modeling of Integrated Circuits session of the 7th International Conference of "Micro- and Nanoelectronics", Tsakhkadzor, Armenia, 2009
- Head of Logic Simulation session of Problems of Developing Advanced Microelectronic Systems Conference, Kiev, Ukraine, 2009
- Head of Leakage Power Models session of 18th International Scientific and Applied Science Conference of Electronics (ET-2009), Sozopol, Bulgaria, 2009

- Member of 032 Board of PhD Dissertation defenses, Yerevan, RA, 2008-present
- Corresponding member of Engineering Academy of RA, Yerevan, RA, 2007-2009
- Member of SEUA Scientific Board, Yerevan, RA, 2007-present
- Member of Curriculum Advisory Board of Synopsys, Inc., 2006-present
- Member of SEUA Scientific-Technical Board, Yerevan, RA, 2006-present
- Head of Program Committee of International Microelectronics Olympiad of Armenia, Yerevan, RA, 2006-present
- Executive Secretary of Educational Awards of the President of RA, Yerevan, RA, 2005-present
- Head of Telecommunication Technologies and IC Design session of Moscow-Bavarian Joint Advanced Student School (JASS), Zelenograd, Russia, 2008-present
- Head of Electronics session of 52nd Conference for Electronics, Telecommunications, Computers, Automatic Control and Nuclear Engineering, Palit, Montenegro, 2008
- Head of Logic Simulation Session of 17th International Scientific and Applied Science Conference of Electronics (ET-2008), Sozopol, Sofia, 2008
- Head of Parameters' Extraction session of Problems of Developing Advanced Microelectronic Systems Conference, Moscow, Russia, 2008
- Member of Program Committee of IEEE East-West Design & Test Symposium (EWDTS'08), Lvov, Ukraine, 2008
- Head of Logic Simulation session of Problems of Developing Advanced Microelectronic Systems Conference, Moscow, Russia, 2007
- Member of Program Committee of 27th International Scientific Conference of Problems of Electronics, Kiev, Ukraine, 2007
- Member of Program Committee of IEEE East-West Design & Test International Symposium (2007), Yerevan, RA, 2007
- Head of Electronics session of 51st Conference for Electronics, Telecommunications, Computers, Automatic Control and Nuclear Engineering, Igalo, Montenegro, 2007
- Head of Educational Track of ArmTech'07 Congress, San-Francisco, CA, USA, 2007
- Head of Leakage Power Models session of 16th International Scientific and Applied Science Conference of Electronics (ET-2007), Sozopol, Bulgaria, 2007
- Head of Logic Simulation session of Problems of Developing Advanced Microelectronic Systems Conference, Moscow, Russia, 2006

Professional experience

2011- present | Head of Microelectronic Circuits and Systems Chair of European Regional Educational Academy, Yerevan, RA

2004-present | Director of Educational Department, Synopsys Armenia CJSC, Yerevan, RA

2001- present | Head of Microelectronic Circuits and Systems Interfaculty Chair of National Polytechnic University of Armenia (NPUA), Yerevan, RA

2001–2004 | VP of University and R&D Program, Director of Educational Department LEDA Design Inc, Yerevan, RA

2002–2004 | Director of Digital and I/O Design Department, LEDA Design Inc. , Yerevan, RA

1994-2001 | Vice Director of Graduate School, SEUA, Yerevan, RA

1989-1991 | Scientific-research worker, Technical University of Vienna, Austria

1985-2001 | Associate Professor, Department of Computer Systems & Informatics, SEUA, Yerevan, RA

1984-1985 | Associate Professor, Department of Computer Systems &Informatics, SEUA, Yerevan, RA

1981-1984 | PhD student, Chair of Electronics of MIFI, Moscow, RF

1978-1981 | Assistant to Professor, Department of Computer Systems & Informatics, SEUA, Yerevan, RA

Research Interests

- System level design and codesign
- IP and platform based design
- Networks-on-Chip design methodologies
- Functional, transaction level, Register Transfer Level and Gate Level modeling and verification
- Electrical, logical and mixed-mode simulation and timing analysis
- Testing, fault modeling and simulation
- Interconnect and package modeling and extraction
- Power analysis and low power IC design
- High speed IC design
- Logic synthesis and circuit optimization
- Signal integrity and design reliability analysis
- Device modeling
- Physical design and manufacturability

Implemented Projects

- Development of Physical Verification runsets for different technology nodes, Yerevan, RA, 2012-present, Head of project
- Development of 32nm Educational Design Kit, Yerevan, RA, 2010-present, Head of project
- Development of 32nm Interoperable Process Design Kit, Yerevan, RA, 2010-present, Head of project
- Development of 90nm Interoperable Process Design Kit, Yerevan, RA, 2009, Head of project
- Modeling, Control and Management of Thermal Effects in Electronic Circuits of the Future, (European Project FP7-ICT-2009-4/248603-THERMINATOR) European Union, Yerevan, RA, 2009-present, Head of project in Armenian office Development of 90nm Educational Design Kit, Yerevan, RA, 2006-2009, Head of project
- Network of integrated circuit design centers in Black Sea region, Black Sea Economic Cooperation (BSEC), Yerevan, RA, 2006-2007, Head of project
- Simulation system of radio-electronic devices, Yerevan, RA, 1995-1996, Head of project
- Simulation and measuring of advanced optoelectronic devices of telecommunication systems (COST-240), Yerevan, RA, 1995-1997, executive developer
- Development of intellectual and portative electro-cardiograph (INTAS-93/75), Yerevan, RA, 1993-1995, executive developer
- Development of algorithms and EDA tools for rad-hard ICs based on fuzzy logic theory, Yerevan-Moscow, 1991-1993, Head of project
- Optimization of SPICE system MOSFET transistor models based on DC measurements, Vienna, Austria, 1989-1991, executive developer
- Extension of logic simulation tool for consideration of parasitic effects of topological realization of VLSI, Yerevan, RA, 1987-1988, Head of project
- Development of VLSI logic simulation tool with consideration of external affects, Yerevan, RA, 1987-1988, Head of project
- Modification of logic simulation tool and union of stages of logic, circuit and topological design, based on databases Yerevan, RA, 1985-1987, Head of project
- Development of specialized monitoring system for checking and screening of variable resistors, Yerevan, RA, 1984-1985, executive developer
- Development of high speed operational amplifiers, mathematical methods and EDA software for ADC and DAC, Moscow, RF, 1983-1984, executive developer
- Development of simulation system for ADC and DAC, Moscow, RF, 1982-1983, executive developer
- Basic educational EDA tool of the University, Moscow, RF, 1981-1982, executive developer
- Development and application of automated control system for air company, Yerevan, RA, 1978-1981, executive developer

Supervision:
- PhD Defenses

N	Name of PhD student	Theme of dissertation	Year of defense
1	Buakel Ziyad	Automated Adaptive Simulation of Electronic Circuits	1996
2	Vatyan Arman	Post-Topological Logic Simulation of Digital Circuits	1998
3	Nazinyan Sergey	Development of Automated Optimization Methods of Timing Characteristics of Digital Circuits for the Reduction of Power Consumption	2000
4	Hovhannisyan Davit	Development of Structural Optimization Methods of Digital Circuits	2004
5	Soghomonyan Vahram	Development of Automated Simulation Methods of High Speed VLSI	2004
6	Sargsyan Sergey	Development of Simulation Methods of Interconnects and Power Buses of High Speed VLSI	2004
7	Mkrtchyan Emil	Development of Simulation Methods of Digital Circuits based on Decomposition	2004
8	Davtyan Davit	Protection of VLSI from ESD Events	2006
9	Fanyan Karen	Development of Methods of Checking Electromigration Rules in VLSI	2006
10	Martirosyan Aram	Development of System Level EDA Methods of VLSI	2007
11	Petrosyan Davit	Circuit Design of VLSI I/Os	2008
12	Shahinyan Tigran	Development of Means for Substrate Noise Consideration Anticipated for VLSI Physical Design	2008
13	Soghomonyan Davit	Development of Characterization Methods for EDA Systems of Digital VLSIs	2008
14	Petrosyan Tigran	Development of Means to Increase Accuracy of High-Speed Models of Submicron Complementary Metal-Oxide-Semiconductor Elements	2009
15	Martirosyan Aram	Development of CAD Design Tools of Single-Chip Computing Network	2009
16	Matevosyan Arshavir	Development of Automated Optimization Methods of Leakage Current	2009

		In VLSI	
17	Chobanyan Samvel	Development of High Speed Means of Automated Design of Sigma-Delta Modulators	2009
18	Shaghgamyan Davit	Design and Research of Auto-Tuning Scheme for I/O cells of High-Speed Integrated Circuits	2009
19	Grigoryan Davit	Research of EDA methods of Digital Integrated Circuits based on Fuzzy Logic	2010
20	Mirzoyan Davit	Development of Analog Integrated Circuit Design Methods with Consideration of Features of Submicron Technologies	2011
21	Stepanyan Artur	Development and Research of Software to Reduce Power Consumption of I/O Cells in VLSI	2011
22	Petrosyan Hayk	Development and Research of Power Consumption Reduction Methods in Integrated Circuits	2012
23	Abovyan Sargis	Development and Research of Semiconductor Multiprocessor Systems	2012
24	Aslanyan Norayr	Development of High-Speed Design Automation Tools for 6 Pipeline Analog to Digital Converters	2012
25	Petrosyan Gor	Development and Research of Automated Timing Verification Tools for Digital Very-Large-Scale Integration Circuits	2012
26	Aharonyan Vahram	Development and Research of Universal Microelectronic Device of Serial Data Transmission	2012
27	Durgaryan Armen	Development of PLL Noise Immunity Improvement Methods	2013
28	Khachatryan Ararat	Development and Research of Means for Designing Protection Structures from Electrostatic Phenomena	2013
29	Musayelyan Eduard	Development and Research of Low Power Digital Cells Library	2013
30	Poghosyan Aram	Development and Research of Automated Design Tool for Integrated Multi-core Processor System	2013
31	Babayan Eduard	Design of Low Power Digital Blocks Embedded in Integrated Circuits	2014
32	Sloyan Karen	Development and Research of Statistical Behavior Reproducing Method of Digital Interated Circuits	2014
33	Gevorgyan Ara	Development of Methods for Thermal Issues Investigation in 3D Integrated Circuits	2014
34	Baghdasaryan Aram	Automated IC Design Parallelization Methods Development and Research	2014
35	Martirosyan Ashot	Development of Methods of Automated Intervalic Logic Design of Digital Circuits	2014
36	Zargaryan Grigor	Development and Research of Software Complex of Hardware/Software Ratio of Universal Serial Bus	2014
37	Mkrtchyan Karen	Development of Automated Design Tools for Regulating Circuits Externally Connected to Very-large-scale Integrated Circuits	2015
38	Balabanyan Abraham	Development of Influence Compensation Methods of Integrated Circuit Operating Condition Variations	2015
39	Hayrapetyan Artak	Development of High Speed Transmitter-Receiver Blocks of Integrated Circuits	2015
40	Nersesyan Nerses	Efuse Drivers Automated Generation for Embedded Systems	2015
41	Babayan Vahe	Research and Development of Power Consumption Reduction Means for Telecommunication Devices	2015
42	Osipyan Hasmik	Development of Automation Design Systems for Homogeneous Device Architectures	2015
43	Movsisyan Karine	Development and Exploration of Means of Stabilization of Integrated Circuits Input/Output Units Characteristics	2015
44	Kasarjyan Hovhannes	Development and Investigation of Automated Physical Design Tools of Analog Integrated Circuits	2016
45	Krrikyan Harutyun	Development and Investigation of Reconfigurable Compuing System Architectures	2016
46	Galstyan Vache	Development and Research of Integrated Power Converter Design Methods	2016
47	Aleksanyan Ani	Development and Research of Self-Calibration Method of Integrated Circuit for Input/Output Devices Termination Resistance Variation Elimination	2016

- More than 160 Master Theses
- More than 295 Diploma Works

Grants
- Winner in the contest of State Committee of Science of the Republic of Armenia for High Efficiency Scientific Worker, 2015
- Winner in the contest of State Committee of Science of the Republic of Armenia for High Efficiency Scientific Worker, 2013
- European Union, 2009-present
- Black Sea Economic Cooperation (BSEC), 2007-2008
- European Interuniversity Microelectronics Center (IMEC, Belgium), 2006-2008
- Osterreichishe Austaushe Dienst fellowship (by Staff Scientific Exchange Service of Austria), 1991-2001

Publications
- 245 scientific publications
- 128 methodical publications and manuals

Personal
Date of birth: January 29, 1956
Place of birth: Yerevan, Armenia
Marital status: Married, 4 children

List of scientific publications

1. Melikyan V.Sh., Abramyan K.G., Janpoladyan B.L., Abramyan A.K. Creation and Introduction to Exploitation on the basis of Information Computer Center, Latvian Republic Organization of Civil Aviation, "Calculation and accounting"// YPI report, No. 81041905, Yerevan, 1981.-P. 77 (in Russian)
2. Melikyan V.Sh., Abramyan K.G., Janpoladyan B.L., Abramyan A.K. Creation and Introduction to Exploitation on the basis of Information Computer Center, Latvian Republic Organization of Civil Aviation, "Calculation and accounting"// YPI report, No. 81041906, Yerevan, 1981.-P. 105 (in Russian)
3. Melikyan V.Sh., Arkhangelskiy A.Y., Karmazinskiy A.N., Petrov G.V. Basic Educational CAD of University // Report of MIFI, No. 01822000176, Moscow, 1982.-P. 105 (in Russian)
4. Melikyan V.Sh., Arkhangelskiy A.Y., Arkhangelskaya I.T., Levshin N.G., Sergienko B.Yu. Software development of simulating analog-digital converters of information// Report of MIFI, No. 01824034446, Moscow, 1983.-P. 196 (in Russian)
5. Melikyan V.Sh., Arkhangelskiy A.Y., Volkov Yu.A., Abramov V.F., Mishin A.A., Levshin N.G. Development of high-speed operational amplifiers, development of mathematical and software provision of CAD of analog-digital microassembly// Report of MIFI, No. 01824021405, Moscow, 1983.-P. 287 (in Russian)
6. Melikyan V.Sh., Arkhangelskiy A.Y. Functionality models of logic cells in the program of mixed-mode logic-circuit simulation// Theses of reports of All-Union Scientific-Technical Conference of "Design Automation of Computers and Systems", Yerevan, 1983.-P. 80-82 (in Russian)
7. Melikyan V.Sh., Arkhangelskiy A.Y. Lavrenov O.E., Rojukalns P.P., Svettsov S.V., Fedorkov B.G. A program of mixed-mode simulation of analog-digital VLSI// Theses of reports of All-Union Conference of "Methods and microelectronic means of digital conversion and signal processing", Riga, 1983.-P. 231-236 (in Russian)
8. Melikyan V.Sh. Logic-circuit simulation of analog-digital nodes of radio metering devices// Proceedings of 9th All-Union Scientific-Technical Conference of "Radio metering", Kaunas, 1983.-P.177-180 (in Russian)
9. Melikyan V.Sh. Description of discrete systems on ELAIS-L language // Theses of reports of 4th All-Union Seminar "Simulation of discrete control and calculating systems", Sverdlovsk, 1984.-P. 64-66 (in Russian)
10. Melikyan V.Sh., Arkhangelskiy A.Y., Levshin N.G. Principles of designing a system of mixed-mode simulation of electronic circuits // Seminar materials of "Design Automation in radioelectronics and electrical engineering", Moscow, 1984.-P. 91-93 (in Russian)
11. Melikyan V.Sh. Mixed-mode simulation of analog-digital VLSI // Dissertation for scientific degree of PhD, MIFI, Moscow, 1984.-P. 233 (in Russian)
12. Melikyan V.Sh., Janpoladyan B.L. Dialogue Teaching System //Interuniversity proceedings of automation, computer engineering and electronics "Transmission and Information Processing", Yerevan, YPI pub.,1984.-P. 9-12 (in Russian)
13. Melikyan V.Sh., Arkhangelskiy A.Y. Mixed-mode circuit and logic simulation of analog digital circuits // Electronic simulation, Vol. 6, N5, Kiev, Ukraine, 1984.-P. 35-39 (in Russian)
14. Melikyan V.Sh., Avetikova E.G. Development of specialized control-measuring system for control and screening a series of resistor variables on the basis of microcomputers // YPI report, No. 01850022861, Yerevan, 1985 (in Russian)
15. Melikyan V.Sh., Arkhangelskiy A.Y. A program of mixed-mode analysis of analog-digital circuits // Proceedings of MIFI "Electrical engineering and devices for experimental physics", Moscow, 1985.-P.134-138 (in Russian)
16. Melikyan V.Sh., Arkhangelskiy A.Y. Macromodels on switched capacitors elements // Radioelectronics. Vol. 29, No. 6, 1986.-P. 86-87 (in Russian)

17. Melikyan V.Sh., Demirkhanyan A.M., Avetikova E.G., Manukyan G.G. Modification of program of logic simulation and union of phases of logic, circuit and layout design on database // YPI report, No. 01860055887, Yerevan, 1987.-P. 85 (in Russian)
18. Melikyan V.Sh., Demirkhanyan A.M., Avetikova E.G., Manukyan G.G. Development of a program of logic simulation for consideration of parasitic effects of topological realization of VLSI // YPI report, No. 01880047950, Yerevan, 1988.-P. 57 (in Russian)
19. Melikyan V.Sh., Demirkhanyan A.M., Avetikova E.G. Development of VLSI logic simulation program with consideration of external affects // YPI report, No. 01880047948, Yerevan, 1988.-P.46 (in Russian)
20. Melikyan V.Sh., Hovasapyan N.O., Manukyan G.G. Definition of noise immunity of digital VLSI // Interuniversity proceedings of YPI "Technical means and mathematical provision of computing systems", Yerevan, 1988.-P. 60-62 (in Russian)
21. Melikyan V.Sh., Ovasapyan N.O., Petrukhin V.P. United system of logic simulation and layout design of VLSI // Automation design in electronics, "Technics", Vol. 40, Kiev, 1989.-P. 61-64 (in Russian)
22. Melikyan V.Sh., Kusheryan E.A. A language of describing digital circuits // Proceedings of Moscow Aviation Institute, Moscow, 1989.-P.17-20 (in Russian)
23. Melikyan V.Sh., Simonyan A.Sh. Consideration of external affects in the program of logic analysis // Interuniversity proceedings of YPI "Technical means and mathematical provision of computing systems", Yerevan, 1990.-P. 61-64 (in Russian)
24. Melikyan V., Mnatsakanian V., Ziad B., Hayrapetyan T. VLSI adaptive simulation// Proceedings of the 1st International conference on Application of Critical Technologies for the Needs of Society, Yerevan, 1995.-P. 135 (in Russian)
25. Melikyan V.Sh., Buakel Z.Kh. Logic model of digital cell for small signals// SEUA. Armenian Scientific-Research Institute of Information, 20.11.95, N 6, Yerevan,1996 (in Russian)
26. Melikyan V.Sh., Soghomonyan S.Kh., Rahanyan R.R., Simonyan A.Sh. A simulation system of radioelectronic devices and calculation of model parameters// SEUA report 93-806, Yerevan, 1996.-P.45 (in Armenian)
27. Melikyan V.Sh, Muradyan M.A., Muradyan M.A., Dashtoyan R.V., Gasparyan E.G. An Elaboration of a Universal, PC Programmable, Portable Device with ITS own Microcomputer for Analyzing Random Signals// Proceedings of the Trans Black Sea Region Symposium on Applied Electromagnetism, Epirus-Hellas, Athens, 1996. -P.BISY 12 (in English)
28. Melikyan V.Sh. Structural and Eventual Decomposition of Large Electronic Schemes// Proceedings of the Trans Black Sea Region Symposium on Applied Electromagnetism, Epirus-Hellas, Athens, 1996.-P. MMWS 7 (in English)
29. Melikyan V.Sh, Bagdasaryan H.V., Nshanyan M.A., Uzunoglu N.K. Computer Modeling of Fiber-optic Cîmmunication Systems// Proceedings of the Trans Black Sea Region Symposium on Applied Electromagnetism, Epirus-Hellas, Athens, 1996.-P. OPSY 9 (in English)
30. Melikyan V.Sh., Mnatsakanyan V.A., Ziad B.Kh. Universal adaptive system of parametrical identification of models of electronic components // Proceedings of international scientific-technical conference "Problems of physical and biomedical electronics", Kiev, 1996.-P. 68-72 (in Russian)
31. Melikyan V.Sh., Muradyan M.A. Software of portative, universal, intellectual, pre-programmable electrocardiograph // Proceedings of international scientific-technical conference ""Problems of physical and biomedical electronics", Kiev, 1996.-P. 73-74 (in Russian)
32. Melikyan V.Sh., Nazinyan S.M. Optimization algorithm of digital paths delays // Proceedings of "Computer Science and Information technologies" International Conference, Yerevan, 1997.-P. 322-325 (in Armenian)
33. Melikyan V.Sh., Harutyunyan A.A. Consideration Algorithm of External Influence's Affect on Digital Circuits Functioning// Proceedings of "Computer Science and Information technologies" International Conference, Yerevan, 1997.-P. 330-333 (in Armenian)
34. Melikyan V.Sh., Avakyan A.S., Amirkhanyan K.G., Manucharyan D.V. Logic simulation of digital circuits on the basis of fuzzy logic//SEUA.- Armenian Scientific-Research Institute of Information, 12.11.96, N 31, Yerevan, 1997 (in Russian)
35. Melikyan V.Sh., Pivazyan T.G. A software complex of parametrical optimization of models of technical objects // SEUA. Armenian Scientific-Research Institute of Information, 11.04.97, N 88, Yerevan, 1997 (in Russian)
36. Melikyan V.Sh., Virabyan L.G., Khoetkhem A.A. Event-trigger decomposition in case of mixed-mode simulation of VLSI // SEUA. Armenian Scientific-Research Institute of Information, 10.06.97, N 140, Yerevan, 1997 (in Russian)
37. Melikyan V.Sh., Virabyan L.G., Oganes P.Sh. Structural decomposition in case of mixed-mode simulation of VLSI // SEUA. Armenian Scientific-Research Institute of Information, 10.06.97, N 141, Yerevan, 1997 (in Russian)
38. Melikyan V.Sh., Vatyan A.O. Interconnections model delays for the logic analysis of TTL circuits // SUAB, Vol. 1, Computer Engineering, Moscow, 1997.-P. 189-198 (in Russian)
39. Melikyan V.Sh., Vatyan A.O., Simonyan A.Sh. Delay models of digital VLSI Interconnects // RAs National Academy of Science and SEUA. Vol. 3, N 3, Yerevan, 1997.-P. 201-205(in Armenian)
40. Melikyan V.Sh., Vatyan A.O. Interconnections model delays for the logic analysis of ECL circuits //SUAB, Vol. 2, Computer Engineering, Moscow, 1997.-P. 187-194 (in Russian)
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Participation in Conferences

1. 13rd student scientific-technical conference of YPI, Yerevan, Armenia, 1977 - 1 paper
2. 17th scientific-technical conference of professors, lecturers, scientific workers and PhDs of YPI, Yerevan, Armenia, 1980 – 1 paper
3. 18th scientific-technical conference of professors, lecturers, scientific workers and PhDs of YPI, Yerevan, Armenia, 1981 - 1 paper
4. International scientific-technical conference "Simulation and Identification of Components and Nodes of Electrical Engineering", Kiev, Ukraine, February 1-3, 1983 - 2 papers
5. 11st All-Union workshop "Contemporary Elementary Base of Computers and Methods of its design with the help of Computers", Simferopol, Ukraine, May, 1983 - 1 paper
6. All-Union scientific-technical conference "Automation of design of computers and systems", Yerevan, Armenia, September, 1983 - 1 paper
7. 9th All-Union scientific-technical conference "Radio metering", Kaunas, Lithuania, October, 1983– 1 paper
8. All-Union conference "Methods and Microelectronic Means of Digital Transformation and Signal Processing", Riga, Latvia, November, 1983 - 1 paper
9. Seminar "Problems of Developing Semiconductor VLSI for CEA", Moscow, RF, January 23-25, 1984 - 1 paper
10. International scientific-technical conference "Simulation and Identification of Components and Nodes of Electrical Engineering", Kiev, Ukraine, January 31-February 2, 1984 - 1 paper
11. Scientific-technical seminar "Design Automation in Radioelectronics and Computer Engineering", Moscow, RF, April 2-4, 1984 - 1 paper
12. 4th All-Union seminar "Simulation of Discrete Control and Computing Systems", Sverdlovsk, RF, September, 1984 - 1 paper
13. International scientific-technical conference Simulation and Identification of Components and Nodes of Electrical Engineering", Kiev, Ukraine, February 5-7, 1985 - 1 paper
14. International scientific-technical conference "Problem Adaptation of Algorithmic and Dataware CAD (Adaptation-86)", Kiev, Ukraine, February 7-9, 1986 - 1 paper
15. Scientific-methodical seminar of professors' staff of YPI "Perfection Ways of Students' Self-Instruction", Yerevan, Armenia, March 16-19, 1987 - 1 paper
16. International scientific-technical conference "Problem Adaptation of Algorithmic and Dataware CAD (Adaptation-88)", Kiev, Ukraine, February 2-4, 1988 - 1 paper
17. All-Union scientific-technical conference "Mathematical and Software Provision of Design Automation in Electronics", Riga, Latvia, March 1-3, 1988 - 1 paper
18. 2nd Interindustry Workshop "System Integration of Training Resources, Design Automation and flexible fabrication of RES", Yerevan, Armenia, October 31-November 4, 1988 - 2 papers
19. International scientific-technical conference "Problem Adaptation of Algorithmic and Dataware CAD (Adaptation-89)", Kiev, Ukraine, January 31-February 2, 1989 - 1 paper
20. All-Union School of Young Scientists and Specialists "Actual Problems of Creating Intellectual CAD CEA and VLSI (VLSI-89)",Simferopol, Ukraine, May, 1989 - 1 paper

21. International scientific-technical conference Problem Adaptation of Algorithmic and Dataware CAD (Adaptation-90)", Kiev, Ukraine, January 30-February 1, 1990 - 1 paper
22. International scientific-technical conference "Problems of Automated Modeling in Electronics", Kiev, Ukraine, February 4-6, 1992 - 1 paper
23. International scientific-technical conference "Problems of Automated Modeling in Electronics", Kiev, Ukraine, February 2-4, 1993 - 1 paper
24. Scientific-technical conference of Professors' Staff in SEUA, Yerevan, Armenia, 1993 - 1 paper
25. Scientific Seminar to sum up financial scientific activities of SEUA, Yerevan, Armenia, 1993 - 1 paper
26. International scientific-technical conference "Problems of Physical and Biomedical Electronics", Kiev, Ukraine, May 27-30, 1994 - 2 papers
27. 2nd International Scientific-Methodical conference "Higher Technical Education: Problems of Master's Program", Kiev, Ukraine, May 18-19, 1995 - 1 paper
28. 1st International Conference on Application of Critical Technologies for the Needs of Society, Yerevan, Armenia, September 14-17, 1995 - 2 papers
29. INTAS researchers' symposium, Yerevan, Armenia, October 10-11, 1995 - 1 paper
30. Trans Black Sea Region Symposium on Applied Electromagnetism, Metsovo, Epirus-Hellas, Athens, Greece, April 17-19, 1996 - 3 papers
31. International scientific-technical conference "Problems of Physical and Biomedical Electronics", Kiev, Ukraine, May 27-30, 1996 - 2 papers
32. Scientific-technical conference of Professors' Staff in SEUA, Yerevan, Armenia, 1996 - 3 papers
33. Scientific-technical conference of Professors' Staff in SEUA, Yerevan, Armenia, 1997 - 3 papers
34. International conference "Computer Science and Information Technologies" (CSIT), Yerevan, Armenia, September 25-29, 1997 - 2 papers
35. International scientific-technical conference "Problems of Physical and Biomedical Electronics", Kiev, Ukraine, May 28-30, 1998 - 2 papers
36. 3rd International scientific-technical conference "New Information Technologies and Systems", Penza, RF, May 29-31, 1998 - 1 paper
37. Scientific-technical conference of Professors' Staff in SEUA, Yerevan, Armenia, 1998 - 4 paper
38. 5th International Conference "Experience of Designing and Application of CAD Systems in Microelectronics" (CADSM'99), Lvov, Ukraine, May 29-31, 1999 - 2 papers
39. International conference "Computer Science and Information Technologies" (CSIT), Yerevan, Armenia, September 25-29, 1999 - 2 papers
40. Scientific-technical conference of Professors' Staff in SEUA, Yerevan, Armenia, 1999 - 1 paper
41. International scientific-technical conference "Problems of Physical and Biomedical Electronics", Kiev, Ukraine, May 28-30, 2000 - 2 papers
42. International conference "Computer Science and Information Technologies" (CSIT), Yerevan, Armenia, 25-29 September, 2001 - 1 paper
43. 4th national conference "Semiconductor Microelectronics", Tsakhkadzor, Armenia, May 29-31, 2003 - 2 paper
44. International conference "Computer Science and Information Technologies" (CSIT), Yerevan, Armenia, September, 2003 - 1 paper
45. 25th International scientific-technical conference "Problems of Electronics", Kiev, Ukraine, April 8-12, 2005 - 1 paper
46. 26th International scientific-technical conference "Problems of Electronics", Kiev, Ukraine, April 10-14, 2006 - 1 paper
47. 7th International scientific-practical conference "Contemporary Information and Electronic Technologies", Odessa, Ukraine, May 22-26, 2006 - 2 papers
48. 2nd All-Russian scientific-technical conference "Problems of Developing Advanced Micro- and Nanoelectronic Systems-2006" (MES-2006), Moscow, RF, October 9-13, 2006 - 2 papers
49. 27th International scientific-technical conference "Problems of Electronics", Kiev, Ukraine, April 12-15, 2007 - 2 papers
50. 8th International scientific-practical conference "Contemporary Information and Electronic Technologies", Odessa, Ukraine, May 21-25, 2007 - 3 papers
51. 51st International Conference on Electronics, Telecommunications, Computers, Automatic Control and Nuclear Engineering (ETRAN'07), Herceg Novi-Igalo, June 4-8, 2007 - 1 paper
52. 5th IEEE East-West Design & Test International Symposium (EWDTS'07), Yerevan, Armenia, September 7-10, 2007 - 3 papers

53. International Conference "Meeting the Growing Demand for Engineers and Their Educators 2010-2020", Munich, Germany, November 9-11, 2007 - 1 paper
54. 6th International conference "Design Automation of Discrete Systems", Minsk, Belarus, November 14-15, 2007 – 1 paper
55. Armenian Technology Congress (ArmTech'07), San Francisco, CA, USA, 2007 - 2 papers
56. 14th International scientific-technical conference of students and PhDs "Radioelectronics, electronics and power engineering", Moscow, RF, February 28-29, 2008 – 1 paper
57. 28th International scientific-technical conference "Problems of Electronics", Kiev, Ukraine, April 15-17, 2008 - 2 papers
58. 9th International scientific-technical conference "Contemporary Information and Electronic Technologies", Odessa, Ukraine, May 19-23, 2008 - 1 paper
59. 7th European Workshop on Microelectronics Education, Budapest, Hungary, May 28-30, 2008 - 2 papers
60. 52nd International Conference on Electronics, Telecommunications, Computers, Automatic Control and Nuclear Engineering (ETRAN'08), Palic, Igalo, June 8-12, 2008 - 2 papers
61. 17th International Scientific and Applied Science Conference "Electronics" (ET2008), Sozopol, Bulgaria, September 24-26, 2008 - 1 paper
62. ArmTech'08 Congress, Yerevan, Armenia, September, 2008 – 1 paper
63. 3rd All-Russian scientific-technical conference "Problems of Developing Advanced Micro- and Nanoelectronic Systems -2008" (MES-2008), Moscow, RF, October 6-10, 2008 - 3 paper s
64. 6th IEEE East-West Design & Test Symposium (EWDTS'08), Lvov, Ukraine, October 9-12, 2008 - 1 paper
65. International Conference on Science, Technology and Education Policy, Hangzhou, China, October 24-25, 2008 - 1 paper
66. 29th international scientific-technical conference "Electronics and nanotechnologies", Kiev, Ukraine, April 14-16, 2009 - 3 papers
67. Design, Automation and Test in Europe 2009 (DATE09), Nice, France, April 20-24, 2009 - 1 paper
68. OpenSPARC Workshop, Brussels, Belgium, April 23-24, 2009 - 1 paper
69. 53rd International Conference on Electronics, Telecommunications, Computers, Automatic Control and Nuclear Engineering (ETRAN'09), Vrnjacka Banja, Serbia, June 15-18, 2009 - 1 paper
70. Semicoductor micro- and nanoelectronics. 7th international conference, Tsakhkadzor, Armenia, July 3-5, 2009 – 3 papers
71. International Conference on Microelectronic Systems Education (MSE'09), San-Francisco, CA, USA, July 25-27, 2009 - 2 papers
72. 7th IEEE East-West Design & Test Symposium (EWDTS'09), Moscow, Russia, September 11-14, 2009 - 3 papers
73. 18th International Scientific and Applied Science Conference "Electronics" (ET2009), Sofia, Bulgaria, September 14-17, 2009 - 1 paper
74. 8th ASEE Global Colloquium on Engineering Education, Budapest, Hungary, October 12-15, 2009 - 1 paper
75. Armenian Technology Congress (ArmTech'09), San Jose, CA, USA, November 5-8, 2009 - 2 papers
76. 4th International Workshop for Design & Test, Riyadh, Saudi Arabia, November 15-17, 2009 - 1 paper
77. 11th Latin-American Test Workshop, Punta del Este, Uruguay, March 28-31, 2010 - 1 ½»Ïáõóáõ
78. 30th international scientific-technical conference "Electronics and nanotechnologies", Kiev, Ukraine, April 13-15, 2010 - 3 papers
79. 8th European Workshop on Microelectronics Education, Darmstadt, Germany, May 10-12, 2010 - 1 paper
80. 54th International Conference on Electronics, Telecommunications, Computers, Automatic Control and Nuclear Engineering (ETRAN'10), Donji Milanovac, Serbia, June 7-10, 2010 - 1 paper
81. 8th IEEE East-West Design & Test Symposium (EWDTS'10), St. Petersburg, Russia, September 17-20, 2010 - 2 papers
82. SoC Design: Development Trends and Problems International scientific-technical conference, Zelenograd, Russia, October 19-21, 2010 - 1 paper
83. International Conference on "State and Perspectives of development of Professional Training and Increase of qualification of Specialists in state participants of CIS towards new directions of developing techniques and technologies, Moscow, Russia, November 12-13, 2010 - 1 paper
84. 7th International Conference on Computer-Aided Design of Discrete Devices (CAD DD'10), Minsk, November 16-17, 2010 - 1 paper
85. 31st International Scientific-Technical Conference on "Electronics and Nanotechnologies", Kiev, Ukraine, April 12-14, 2011 – 3 papers
86. International conference "Moscow-Bavarian Joint Advanced Student School", Zelenograd, Russia, March 21-25, 2011 - 3 papers

87. 55th International Conference on Electronics, Telecommunications, Computers, Automatic Control and Nuclear Engineering (ETRAN'11), Banja Vrućica, Serbia, June 6-9, 2011 - 2 papers

88. 8th International Conference of "Semiconductor Micro- and Nanoelectronics", Yerevan, Armenia, July 1-3, 2011 - 2 papers

89. ASIA Power Architecture Conference, Shenzhen, China, September 1, 2011 - 1 paper

90. 9th IEEE East-West Design & Test Symposium (EWDTS'11), Sevastopol, Ukraine, September 9-12, 2011 - 1 paper

91. Armenian Technology Congress (ArmTech'11),Yerevan, Armenia, October 10-12, 2011 – 1 paper

92. International conference "Education, Science and Economics at Universities. Integration to International Educational Area", Yerevan, Armenia, September 26-30, 2011 - 1 paper

93. 6th IEEE International Design and Test Workshop (IDT) in Conjunction with IEEE International Conference on Electronics, Circuits and Systems (ICECS), Beirut, Lebanon, December 10-14, 2011 - 1 paper

94. 4th Small Systems Simulation Symposium 2012, Nis, Serbia, February 12-14, 2012 - 1 paper

95. Design, Automation & Test in Europe (DATE'12) conference, Dresden, Germany, 2012, March 12-16, 2012 - 1 paper

96. Interdisciplinary Engineering Design Education (IEDEC) conference, Santa Clara, CA, USA, March 19, 2012 - 1 paper

97. 32nd International Scientific Conference Electronics and Nanotechnology (ELNANO 2012), Kiev, Ukraine, April 10-12, 2012 - 2 papers

98. 9th European Workshop on Microelectronics Education (EWME 2012), Grenoble, France, May 9-11, 2012 - 2 papers

99. 10th IEEE East-West Design & Test Symposium (EWDTS'12), Kharkov, Ukraine, September 14-17, 2012 - 1 paper

100. 5th All-Russian scientific-technical conference "Problems of Developing Advanced Micro- and Nanoelectronic Systems -2012" (MES-2012), Moscow, Russia, October 8-12, 2012 - 1 paper

101. 56th Electronics, Telecommunications, Computers, Automatic Control and Nuclear Engineering (ETRAN) Conference, Zlatibor, Serbia, 2012 - 1 paper

102. Interdisciplinary Engineering Design Education (IEDEC) Conference, Santa Clara, CA, USA, March 4-5, 2013 - 2 papers

103. University/Industry Cooperation-Accelerating Innovation Scientific-Educational Conference, Yerevan, Armenia, May 15, 2013 - 1 paper

104. 9th International Conference of "Semiconductor Micro- and Nanoelectronics", Yerevan, Armenia, 2013 - 2 papers

105. IEEE 33rd International Scientific Conference Electronics and Nanotechnology (ELNANO), Kiev, Ukraine, 2013 - 1 paper

106. Computer Science and Information Technologies Conference (CSIT), Yerevan, Armenia, 2013 - 2 papers

107. 11st IEEE East-West Design & Test Symposium (EWDTS'13), Rostov-on-Don, Russia, 2013 - 4 papers

108. 21st IFIP/IEEE International Conference on Very Large Scale Integration (VLSI-SoC), Novotel, Istanbul, Turkey, October 7-9, 2013 - 1 paper

109. 57th Electronics, Telecommunications, Computers, Automatic Control and Nuclear Engineering (ETRAN) Conference, Zlatibor, Serbia, June 3-6, 2013 - 2 papers

110. Asia-Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PrimeAsia 2013), India, Visakhapatnam, December 19-21, 2013 - 1 paper

111. 5th Small Systems Simulation Symposium 2014, Niš, Serbia, February 12-14, 2014 - 2 papers

112. 12nd IEEE East-West Design & Test Symposium (EWDTS'14), Kiev, Ukraine, September 26-29, 2014 - 3 papers

113. 6th All-Russian scientific-technical conference "Problems of Developing Advanced Micro- and Nanoelectronic Systems -2014" (MES-2014), Moscow, Russia, October 1-3, 2014 - 1 paper

114. 6th Small Systems Simulation Symposium 2016, Niš, Serbia, February 12-14, 2016 – 1 paper